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01/09/02

J1057 U.S. PTO

Attorney's Docket No. MP0092

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATION UNDER 37 C.F.R. 1.10

"Express Mail" Mailing Label Number: EL 898555281 US

Date of Deposit: 1/9/02

I hereby certify that this patent application is being deposited with the United States Postal Service on this date in an envelope as "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Washington, DC 20231.

1/9/02

Suzanne M. Forno
 Suzanne M. Forno

APPLICATION TRANSMITTAL

Box Patent Application

Commissioner for Patents
 Washington, DC 20231

Inventor(s): Sheredy, Joseph

J1046 U.S. PTO
 10/042548
 01/09/02

For: **METHOD AND APPARATUS FOR TESTING A SYSTEM ON A CHIP (SOC) INTEGRATED CIRCUIT COMPRISING A HARD DISK CONTROLLER AND READ CHANNEL**

Enclosed are the following items:

- A. ☒ Postcard
 B. ☒ Patent Application Data Entry Format
 C. ☒ Other (specify)
 ☐ Form PTO 1595
 ☒ Form PTO 1449
 ☒ Information Disclosure Statement

- D. ☒ Declaration and Power of Attorney
 ☒ executed

E. ☒ Application Total Pages: 19F. ☒ Drawings Total Sheets: 3

☐ Certified copies of the priority documents have been filed in the parent of this continuing application. The parent application, filed , was assigned Serial No. .

☐ Cancel in this application original claims ___ of the prior application before calculating the filing fee. (At least one original independent claim is retained for filing.)

- ☐ Applicant(s) presently intend(s) to file additional papers in this case after receiving an official Filing Receipt. Should the Examiner take this case up for action before receiving such papers, it is respectfully requested that the Examiner contact the attorneys for the applicant(s) at the telephone number shown below.
- ☐ A petition, fee and response has been filed, or a conditional petition is now being filed, in the prior application to extend the term of the pending prior application. Enclosed is a petition for extension of time in the prior application.

CLAIMS AS FILED (OTHER THAN SMALL ENTITY)

CLAIMS	(1) For	(2) Number Filed	(3) Number Extra	(4) Rate	(5) Calculations
	Total Claims (37 CFR 1.16(c))	32 - 20 =	12	x \$18=	\$ 216.00
	Independent Claims (37 CFR 1.16(b))	6 - 3 =	3	x 84=	\$ 252.00
	Multiple Dependent Claims (if applicable) (37 CFR 1.16(d))			+ \$ 280 =	\$0
	Provisional Application Filing Fee = (37 CFR 1.16(k))				\$
	Basic Fee = (37 CFR 1.16(a))				\$740.00
	TOTAL =				\$1,208.00

- ☒ Applicant(s) believe(s) that no extension of time is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.


The Commissioner is hereby authorized to charge and credit the credit card authorized in the attached Submission Of Credit Card Payment:

- ☒ Charge the total filing fee of \$ \$1,208.00 to the credit card authorized in the attached Submission Of Credit Card Payment.
- ☒ Charge any additional filing fees as required under 37 C.F.R. 1.16 and 1.17 to the credit card authorized in the attached Submission Of Credit Card Payment.
- ☒ Credit any overpayment to the credit card authorized in the attached Submission Of Credit Card Payment.

☒ **Request Not To Publish (SB35)**

Please address all correspondence in connection with this application to:

Customer Number: 23624
Patent Department
Marvell Semiconductor, Inc.
645 Almanor Avenue
Sunnyvale, CA 94085


Eric B. Janofsky

Registration No.: **30,759**
 Telephone: (408) 222-2500
 FAX: (408) 328-0918

Date: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sheredy, Joseph

Group Art Unit: Not Yet
Assigned

Serial No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: Herewith

Title: Method and Apparatus for Testing a System on a Chip (SOC) Integrated
Circuit Comprising a Hard Disk Controller and Read Channel**REQUEST AND CERTIFICATION
UNDER 35 U.S.C. 122(b)(2)(B)(i)**Commissioner for Patents
Washington, D.C. 20231

Sir:

I hereby certify that the invention disclosed in the attached application **has not been and is not intended, at this time, to be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

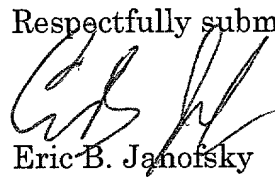
I hereby request that the attached application not be published under 35 U.S.C. 122(b).

Applicant reserves the right to subsequently file an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing. At that time, Applicant intends to notify the United

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States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application.

Respectfully submitted,

 1/9/02

Eric B. Janofsky
Attorney for Applicant
Registration No. 30,759

Date:

20050701 31524001

MP0092

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sheredy, Joseph

Group Art Unit: Not Yet
Assigned

Serial No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: Herewith


Title: Method and Apparatus for Testing a System on a Chip (SOC) Integrated
Circuit Comprising a Hard Disk Controller and Read Channel

SUBMISSION OF CREDIT CARD PAYMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Please charge fees associated with the subject filing to the following:
American Express Corporate Credit Card Account #: 3785 471287 71007
Credit Card Expiration Date: 04/02
Name as it Appears on Credit Card: Eric B. Janofsky
Payment Amount in U.S. Dollars: \$40.00

Signature: 

Date: 

Credit Card Billing Address:

Marvell Semiconductor, Inc.
645 Almanor Avenue
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Respectfully submitted, 

Eric B. Janofsky
Attorney for Applicant
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Date: 

206070-84524007